

### **REMARKS**

The allowance of claims 1-9 and 11-22 is acknowledged.

Page 33 of the Specification has been corrected as suggested by the Examiner, and in addition, a typographical error has been corrected. Also, new Figs. 3 and 13 are submitted herewith, which make the corrections suggested by the Examiner.

The Examiner has also objected to the drawings in the belief that "a generator," as claimed in claims 6 and 8 is not shown in the drawings. As noted by the Examiner, Figs. 13 and 25 illustrate error detectors, and claims 6 and 8 have been amended to change "generator" to "error detector." Such should satisfy this objection of the Examiner.

The Examiner has also objected to the use of the term "signal conversion" in claim 10 as lacking antecedent basis under 35 U.S.C. § 112 and as not being shown in the drawings. It is believed that the meaning of "signal conversion" in the claims is clear from the specification as referring to, for example, selector 230 shown in Fig. 18, installed in the corrector 200 shown in Fig. 17. This "conversion" step converts the reverse-gamma correction-applied first video signal having a first graduation level, such as 496 graduation levels mentioned at page 29, line 26, into a second video signal having a specific number of bits, such as 8 bits mentioned at page 29, line 37, or a second graduation level, such as 256 graduation levels mentioned at page 32, lines 29 and 30.

Based on the foregoing, reconsideration of the objection/rejection of claim 10 is requested, and the issuance of a Notice of Allowance of claims 1-22 is earnestly solicited.

If any further issues remain after this amendment, a telephone call to the undersigned would be appreciated.

Respectfully submitted,



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